Fig. 1
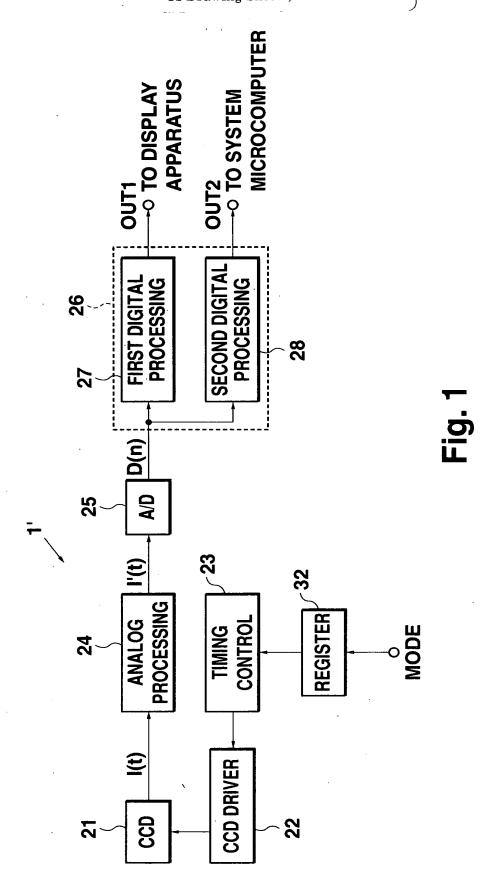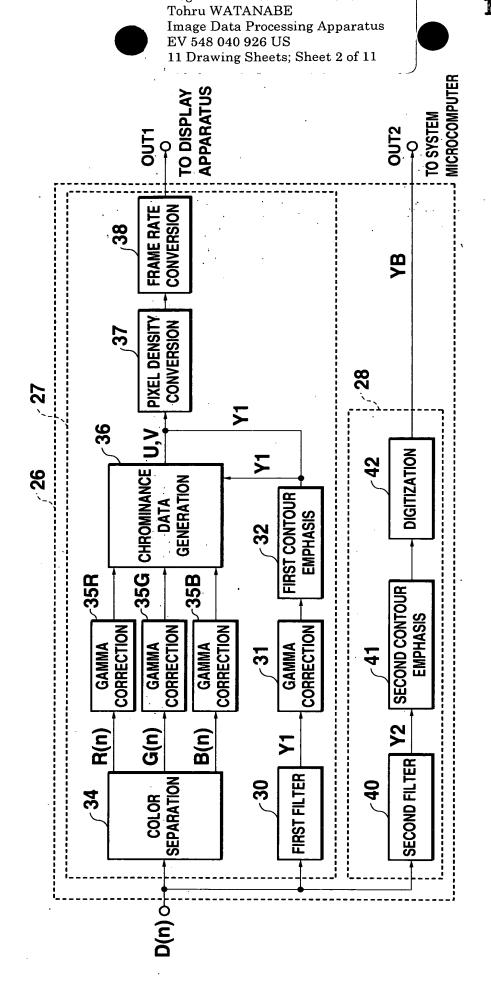
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Fig. 2

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BEFORE CONVERSION

D(1)	D(2)	D(3)	D(4)	D(5)	D(6)	D(7)	D(8)

AFTER CONVERSION

D'(1)	D'(2)

Fig. 3

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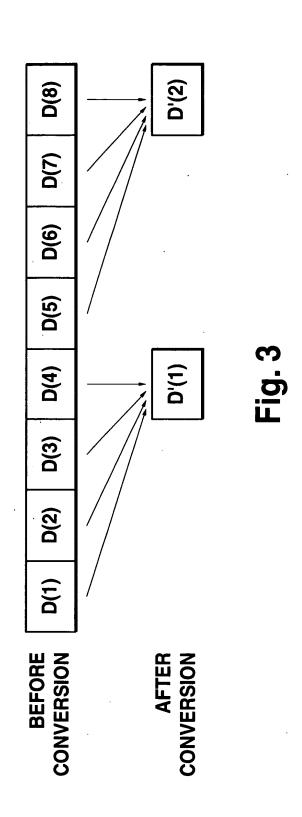EV 548 040 926 US
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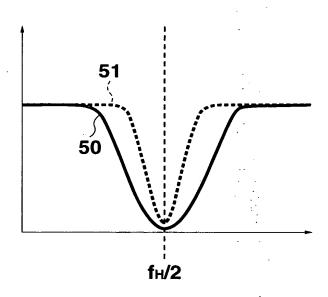
51

50

$f_H/2$

Fig. 4

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Fig. 5

Fig. 6

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Fig. 7

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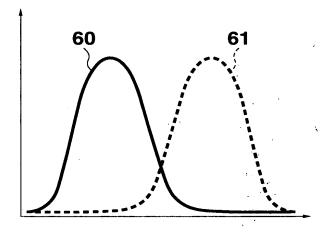EV 548 040 926 US
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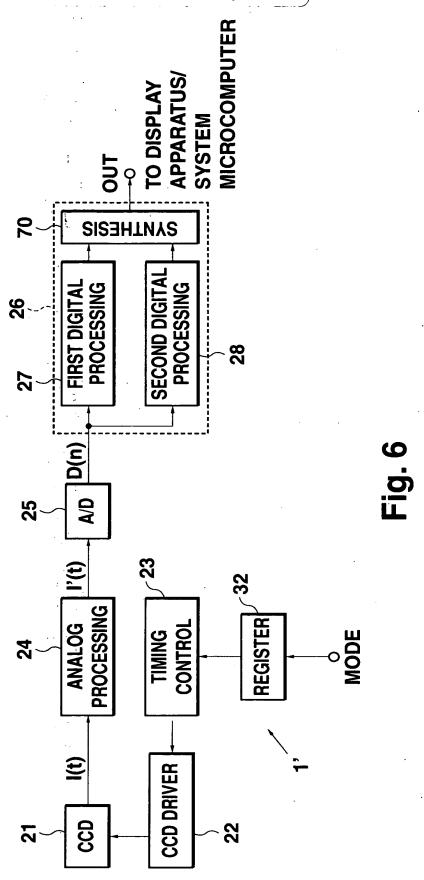
Fig. 8

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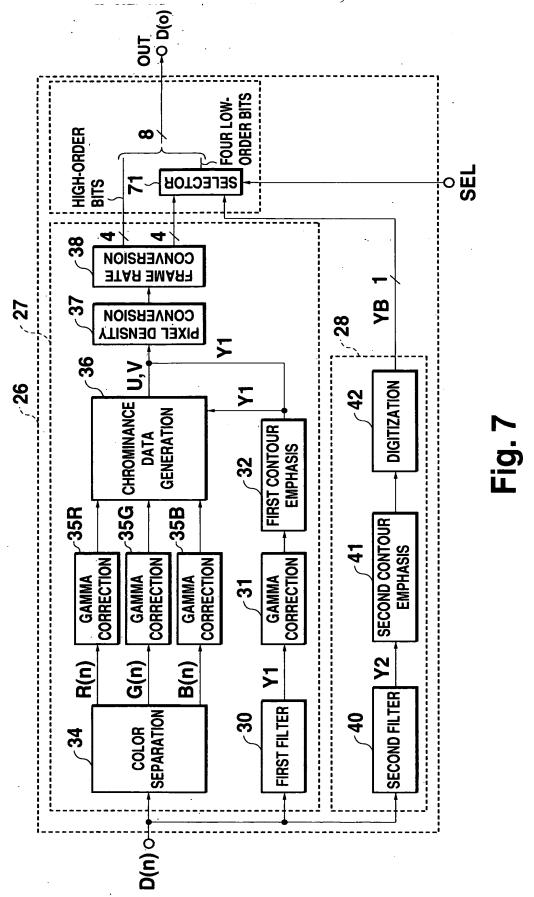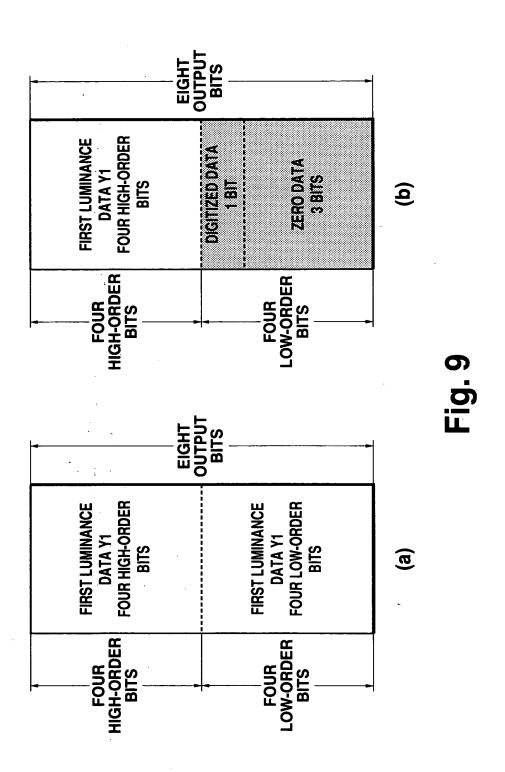**EIGHT OUTPUT BITS**

FOUR HIGH-ORDER BITS — FIRST LUMINANCE DATA Y1 FOUR HIGH-ORDER BITS

DIGITIZED DATA 1 BIT

FOUR LOW-ORDER BITS — ZERO DATA 3 BITS

(b)

EIGHT OUTPUT BITS

FOUR HIGH-ORDER BITS — FIRST LUMINANCE DATA Y1 FOUR HIGH-ORDER BITS

FOUR LOW-ORDER BITS — FIRST LUMINANCE DATA Y1 FOUR LOW-ORDER BITS

(a)

Fig. 9

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	t0	t1	t2	t3	t4	t5	t6

BEFORE CONVERSION: D(1)u D(2)u D(3)u D(4)u D(5)u D(6)u D(7)u D(8)u D(9)u D(10)u D(11)u D(12)u

FOUR HIGH-ORDER BITS: ZERO DATA | D'(1)u | ZERO DATA | D'(2)u | ZERO DATA | D'(3)u

FOUR LOW-ORDER BITS: ZERO DATA | D'(1)s | ZERO DATA | D'(2)s | ZERO DATA | D'(3)s

DIGITIZED DATA: D(1)b D(2)b D(3)b D(4)b D(5)b D(6)b D(7)b D(8)b D(9)b D(10)b D(11)b D(12)b

SE

OUTPUT DATA: ZERO DATA | D'(1)u / D'(1)s | ZERO DATA | D'(2)u | ZERO DATA | D'(3)u / D(5)b D(6)b D(7)b D(8)b D(9)b D(10)b D(11)b D(12)b

Fig. 10

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IMAGING APPARATUS ~1

SYSTEM MICROCOMPUTER ~3

BARCODE PROCESSING ~3a

DISPLAY APPARATUS ~2

MEMORY ~4

Fig. 11